IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS

1. (Currently Amended) A circuit arrangement for forming the termination of an analog subscriber line, comprising:

~~which has~~ a first connection and a second connection for the subscriber line, ~~and where~~ the first connection ~~is~~ being connected to the second connection by a series circuit comprising a first capacitor, at least one variable resistor and a second capacitor[[,]];

a DC source ~~being~~ connected to ~~the~~ a first node ~~point~~ between the first capacitor and the variable resistor; and

a DC operating point setting circuit connected to the variable resistor and configured to adjust the variable resistor based on DC voltage values measured at the first node and at a second node located between the second capacitor and the variable resistor.

2. (Currently Amended) The circuit arrangement as claimed in claim 1, wherein the DC source ~~has~~ comprises a transistor and a resistor, the load path of the transistor being connected in series with the resistor, and the control connection of the transistor being set by a control circuit.

3. (Currently Amended) The circuit arrangement as claimed in claim 2, wherein the control circuit ~~has~~ comprises an operational amplifier whose output is connected to the control connection of the transistor of the DC source, whose negative input is connected to ~~the~~ a junction point between the transistor and the resistor of the DC source and whose positive input has a reference voltage applied to it.

4. (Currently Amended) The circuit arrangement as claimed in claim 2 wherein the DC source ~~has~~ comprises a diode which is connected in series with the load path of the transistor.

5. (Currently Amended) The circuit arrangement as claimed in claim 3 wherein the DC source ~~has~~ comprises a diode which is connected in series with the load path of the transistor.

6. (New) The circuit arrangement as claimed in claim 1, further comprising an AC terminal impedance control circuit connected to the variable resistor and configured to adjust the variable resistor based on AC voltage values measured at the first and second nodes.